SRM UNIVERSITY DELHI-NCR, SONEPAT

SRM University Haryana, part of the SRM Group, was established in 2013 in Sonepat, Haryana. Offering diverse undergraduate, postgraduate, and doctoral programs, the university focuses on engineering, management, science, humanities, and law. Accredited with an impressive A+ grade by the National Assessment and Accreditation Council (NAAC), it emphasizes research, innovation, and robust industry-academia collaboration. The university actively collaborates with industries and international institutions to enhance practical exposure and industry relevance for students. The campus provides modern infrastructure, including classrooms, laboratories, and libraries. The university's placement cell aids in career guidance and job opportunities, fostering a holistic learning environment

DEPARTMENT OF ELECTRONICS AND COMMUNICATION ENGINEERING

The Department of Electronics and Communication Engineering was established in 2013. The department offers B.Tech. Program at UG level, M.Tech. Program at PG level and Ph.D. program in various specializations. State of the Art laboratories have been established and the department is committed to providing active hands-on and experiential learning to the students of all the programs and supporting the research. The Department also ensures the quality delivery in the classroom teaching through the highly talented and motivated faculty members. The faculty members of the department deliver the fundamental concepts along with the latest trends in the area to strengthen the learning process and to invoke critical thinking and problem-solving skills among the students so that the students can take the challenge to give the solution of the technical and social problem through technology.

OBJECTIVE OF THE FDP

Semiconductor Technology and modern chip design play a vital role in developing various electronic devices ranging from smartphones and computers to medical equipment, sensors and, automotive systems, etc. Design, and development of Modern ICs need the knowledge and skills in the rapidly evolving field of circuit design, semiconductor design, and manufacturing. This program would likely provide faculty members, research scholars, and postgraduate students deep insight into the latest trends, advancements, and challenges related to emerging nanoscale semiconductor devices, advanced semiconductor sensors, and cutting- edge IC design technology and methodologies. In addition, this program aims to equip the participants with hands-on experience in the state-of-the-art tools for the design, analysis, and investigation of high-performance nanoscale semiconductor devices and circuits, which support analog and digital IC design.

OUTCOME OF THE FDP

FDP plays a crucial role by helping the India Semiconductor Mission (ISM) to educate and foster workforce development in semiconductor design, fabrication, sensors design, and modern IC design. FDP empowers participants with the latest knowledge, design tools, and techniques in these fields. FDP also opens up the path for research opportunities, professional networking with peers, and institutional growth. Hands-on or practical sessions equip participants with new skills and expertise in nanoscale semiconductor devices, sensor technology, and IC design. Also, the interactions between participants with industry experts and research scientists provide deep insight into real-world applications, challenges, and opportunities. This FDP also in line with the initiatives of the Government of India such as Make in India, Aatmanirbhar Bharat, Skill India, Startup India, Digital India, and Vikshit Bharat.

ONE WEEK FACULTY DEVELOPMENT PROGRAM (FDP) ON

Semiconductor Technology and Chip Design: Current Landscape, Challenges and Growth Opportunities

28th July 2025 to 1st August 2025

Mode of FDP
ONLINE

Organized by:

Department of Electronics and Communication Engineering
Faculty of Engineering and Technology
SRM University Delhi-NCR, Sonepat, Haryana39, Rajiv Gandhi Education City, Delhi-NCR, Sonepat-131029, Haryana (India)

RESOURCE PERSONS

1. Dr. Indrajit Chakraborty
   Professor,
   Electronics &Electrical Communication
   Engineering Department
   IIT Kharagpur
2. Dr. Satish Kumar Dubey
   Associate Professor
   SENSE, IIT Delhi
3. Dr. Chestor Rebeiro
   Professor
   CSE Department
   IIT Madras

HIGHLIGHTS

1. The sessions will be held online from 10:30 AM to 12:30 PM.
2. The total number of seats is limited to 50.
3. The certificates shall be issued to those participants who have attended the programme with 100% attendance
4. Registration Fee of Rs. 500.00 will be charged. However, for internal faculty/students, it is waived off.
5. Interested candidate may deposit their fee (bank details given below) and can register on the Google link below.
6. Deadline for Registration: 25th July 2025
7. Share the transaction slip through email on satyendra.s@srmuniversity.ac.in
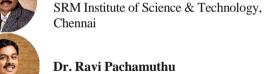
REGISTRATION LINK AND ACCOUNT DETAILS

https://forms.gle/J244z2pk1VHNizMw6

Account Name: SRM EDUCATION & RESEARCH INSTITUTE
Account Type: CURRENT ACCOUNT
Account No: 12330100200333
IFSC CODE: FDRL0002116
Bank Name: FEDERAL BANK LIMITED
Branch Address: KUNDLI, HARYANA-131028

CHIEF PATRONS

Dr. T. R. Paari Vendhar
Hon'ble Founder Chancellor
SRM Institute of Science & Technology, Chennai

Dr. Ravi Pachamuthu
Hon'ble Chancellor
SRM University Delhi - NCR, Sonepat

PATRONS

Prof. (Dr.) Paramjit S. Jaswal
Hon'ble Vice Chancellor
SRM University Delhi – NCR, Sonepat

Prof. (Dr.) V. Samuel Raj
Dean Academic Affairs & Registrar
SRM University Delhi – NCR, Sonepat

MENTOR

Prof. (Dr.) Ranjit Roy
Dean Engineering and Technology
SRM University, Delhi-NCR, Sonepat

TARGETED PARTICIPANTS

The program is open to the faculty members of AICTE/ UGC approved Engineering/ Institutes/Universities, Ph.D. scholars, and Undergraduate/Post Graduate Students.

ORGANIZING COMMITTEE

CONVENOR
Dr. Pawan Kumar Singh
Head,
Department of Electronics and Communication Engineering

COORDINATORS

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